

Customer No.: 31561
Docket No.: 12898-US-PA
Application No.: 10/709,430

AMENDMENTS

To the Specification:

[0015] In an embodiment of the present invention, when the identification module includes a plurality of identification units, each of the plurality of identification units includes at least a control input ~~terminal~~^{terminals}, an output terminal and a plurality of data input terminals, wherein the plurality of data input terminal is coupled to a corresponding one of a plurality of memory ~~units~~^{unit} respectively, and the control input terminal is coupled to a corresponding one of the plurality of control lines respectively. The identification unit is responsive to the control signal received from the plurality of control signals for determining and outputting a content stored in at least one of the plurality of memory units via the output terminal. In an embodiment of the present invention, each of the identification units comprises a plurality of AND gates and a NOR gate. Each of the AND gates includes a plurality of AND gate input terminals and a AND gate output terminal. One of the AND gate input terminals is coupled to one of the data input terminals, and the other AND gate input terminals are coupled to the control input terminal. The NOR gate includes a plurality NOR gate input terminals and a NOR gate output terminal. Each of the AND gate output terminals is connected to one of the NOR gate input terminals, and the NOR gate output terminal is the output terminal of the identification unit.